



Case No. 10599-130

**DECLARATION FOR PATENT APPLICATION**

I, a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled Circuit and Method for Enhanced Low Frequency Switching Noise Suppression in Multilayer Printed Circuit Boards Using A Chip Capacitor Lattice, the specification of which:

- ☐ is attached hereto.  
☒ was filed on March 17, 2004 as Application Serial No. 10/802,664.  
☐ and was amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the patentability as defined in Title 37, Code of Federal Regulations, § 1.56(a).

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate or § 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

Priority Claimed

☒ Yes ☐ No

\_\_\_\_\_  
(Number) (Country) (Day/Month/Year Filed)

I hereby claim the benefit under 35 U.S.C. § 119(e) of any United States provisional application(s) listed below:

\_\_\_\_\_  
(Application Serial No.) (Filing Date)

I hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s), or § 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR § 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application:

\_\_\_\_\_  
(Application Serial No.) (Filing Date) (Status-patented, pending, abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Inventor's Signature

Full name of sole or first inventor

Residence

Citizenship

Post Office Address

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Date: 7-14-04

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Inventor(s): William E. McKinzie, III and Shawn D. Rogers  
 Title: Circuit and Method for Enhanced Low Frequency Switching Noise Suppression in Multilayer Printed Circuit Boards Using A Chip Capacitor Lattice

**POWER OF ATTORNEY**

The specification of the above-identified patent application:

☒ is attached hereto  
☐ was filed on \_\_\_\_\_ as application Serial No. \_\_\_\_\_

I hereby revoke all previously granted powers of attorney in the above-identified patent application and appoint the following attorneys to prosecute said patent application and to transact all business in the Patent and Trademark Office connected therewith:

Rodney A. Daniel - 31,605  
 John G. Rauch - 37,218  
 Anthony P. Curtis, Ph.D. - 46,193

Please address all correspondence and telephone calls to Anthony P. Curtis, Ph.D. in care of:

Brinks Hofer Gilson & Lione  
 P.O. Box 10395  
 Chicago, IL 60610  
 (312)321-4200

The undersigned hereby authorizes the U.S. attorneys named herein to accept and follow instructions from Rodney A. Daniel and Steven Grossman as to any action to be taken in the Patent and Trademark Office regarding this application without direct communication between the U.S. attorney and the undersigned. In the event of a change in the persons from whom instructions may be taken, the U.S. attorneys named herein will be so notified by the undersigned.

Etenna Corporation, a corporation, certifies that it is the assignee of the entire right, title and interest in the patent application identified above by virtue of either:

- ☒ An assignment from the inventor(s) of the patent application identified above, a copy of which is attached hereto.  
 OR  
☐ An assignment from the inventor(s) of the patent application identified above. The assignment was recorded in the Patent and Trademark Office at Reel \_\_\_\_\_, frame \_\_\_\_\_.  
 OR

- ☐ A chain of title from the inventor(s), of the patent application identified above, to the current assignee as shown below:

1. From \_\_\_\_\_ To: \_\_\_\_\_  
 The document was recorded in the Patent and Trademark Office at Reel \_\_\_\_\_, frame \_\_\_\_\_, or a copy thereof is attached.
2. From \_\_\_\_\_ To: \_\_\_\_\_  
 The document was recorded in the Patent and Trademark Office at Reel \_\_\_\_\_, frame \_\_\_\_\_, or a copy thereof is attached.

☐ Additional documents in the chain of title are listed on a supplemental sheet.

The undersigned has reviewed the assignment or all the documents in the chain of title of the patent application identified above and, to the best of undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Signature

Name:

Title:

Rev. Dec.-99  
 Document 5

Date:

7.14.04

David T. Auckland

Chief Technical Officer